

FIG. 1

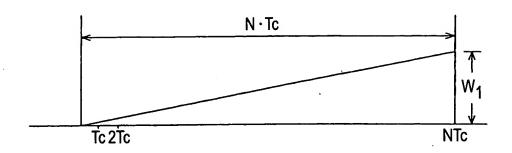


FIG. 2a

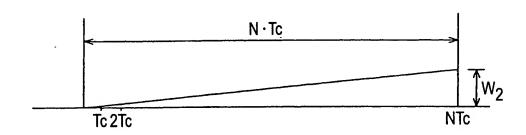
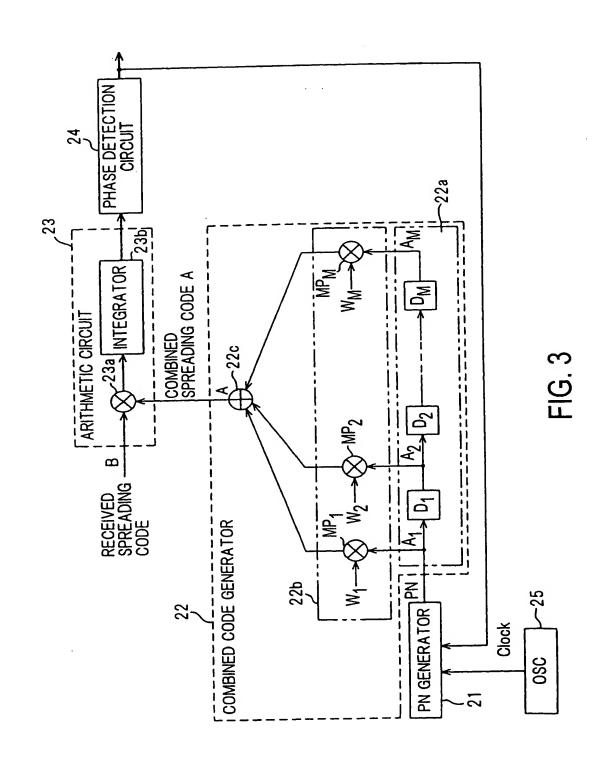


FIG. 2b



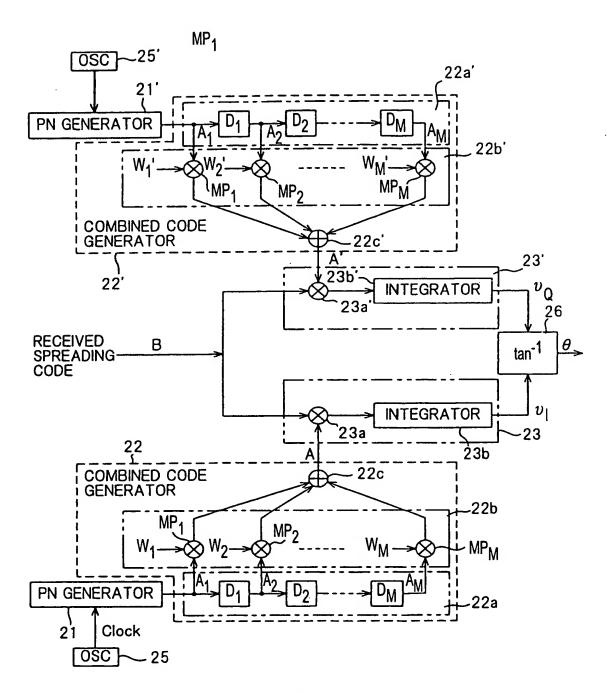


FIG. 4

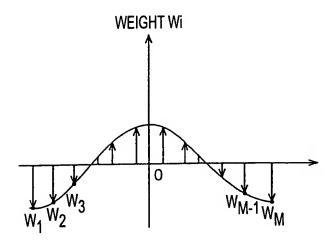


FIG. 5a

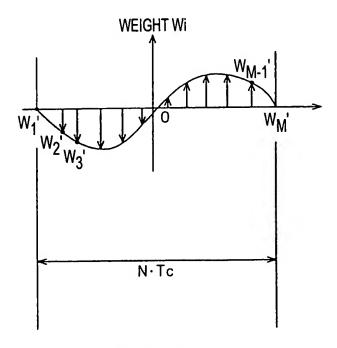


FIG. 5b

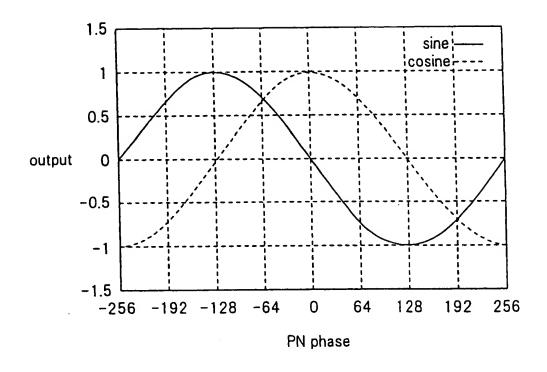


FIG. 6

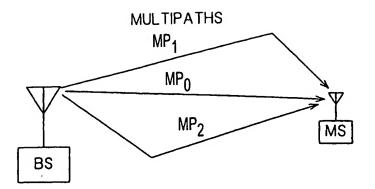


FIG. 7a

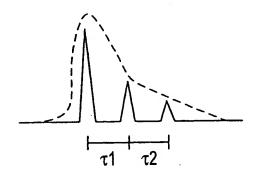


FIG. 7b

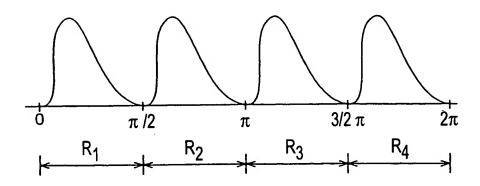


FIG. 7c

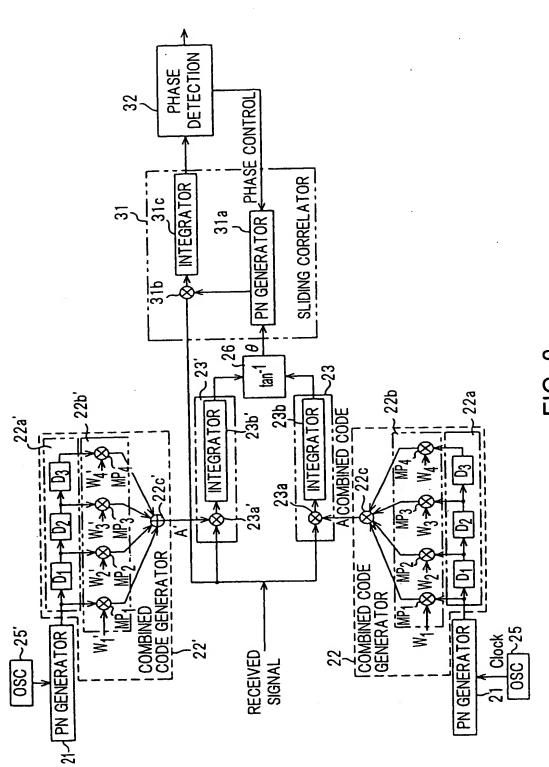
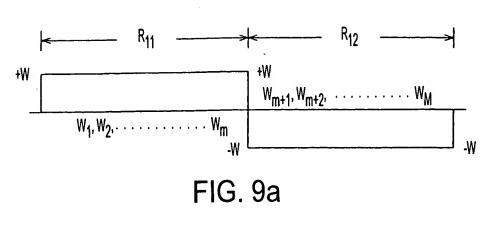


FIG. 8



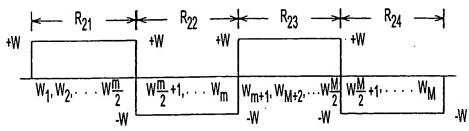


FIG. 9b

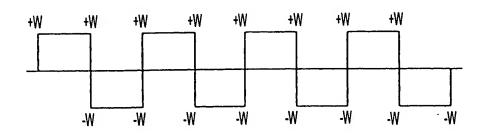


FIG. 9c

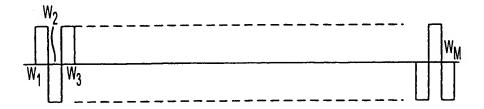
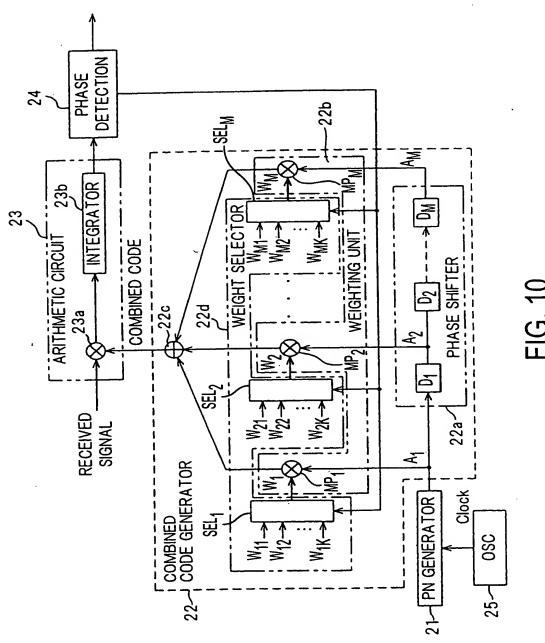
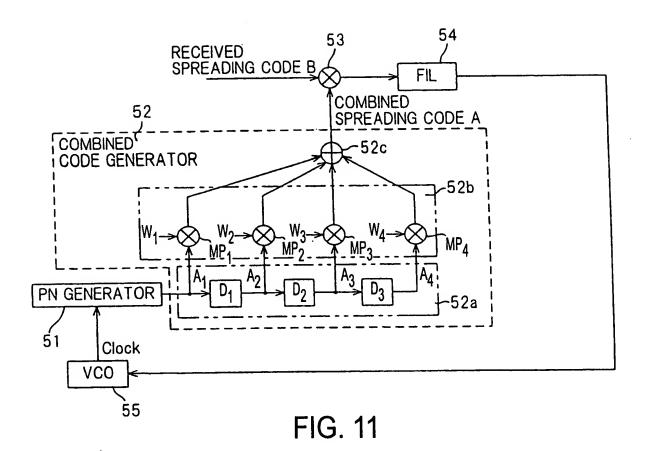


FIG. 9d





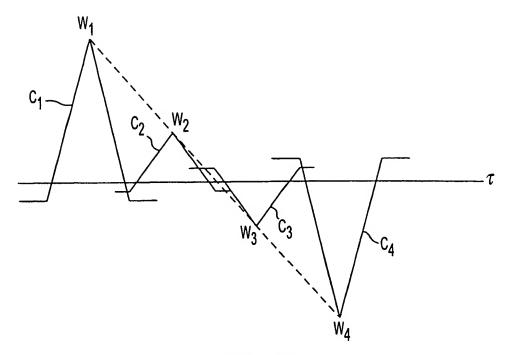


FIG. 12

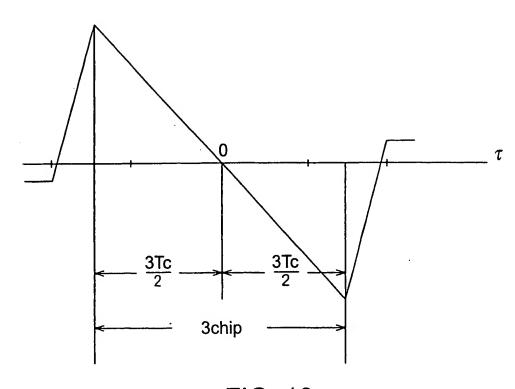


FIG. 13

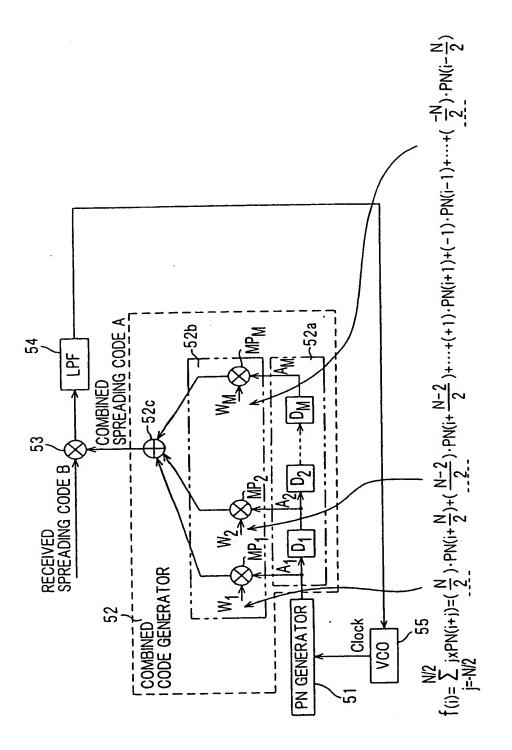


FIG. 14

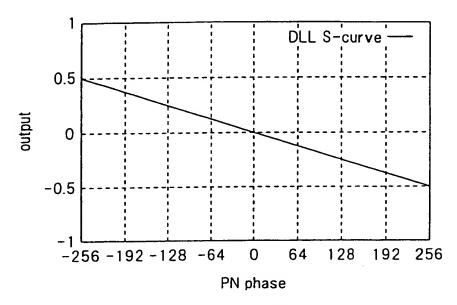


FIG. 15

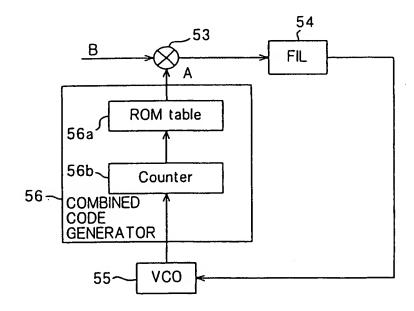


FIG. 16

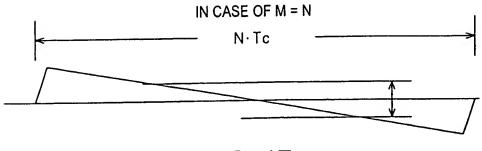
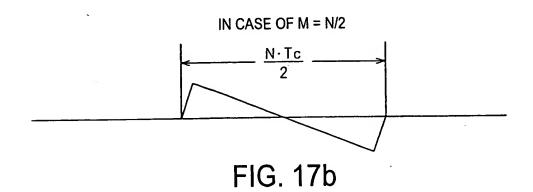


FIG. 17a



IN CASE OF M = 2

1 chip=Nc

FIG. 17c

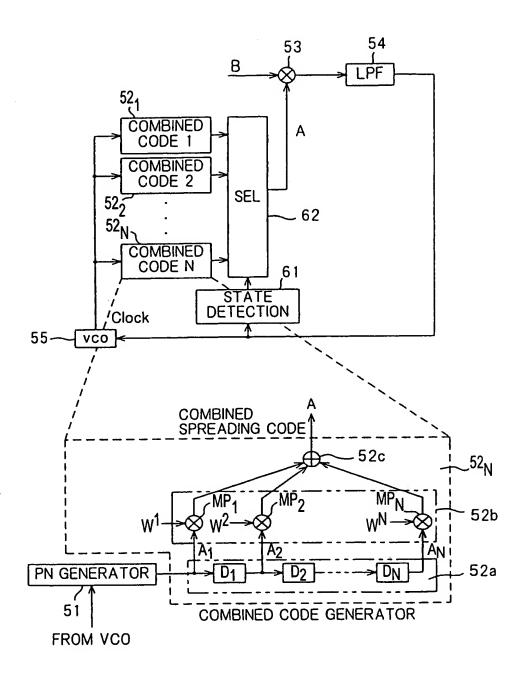
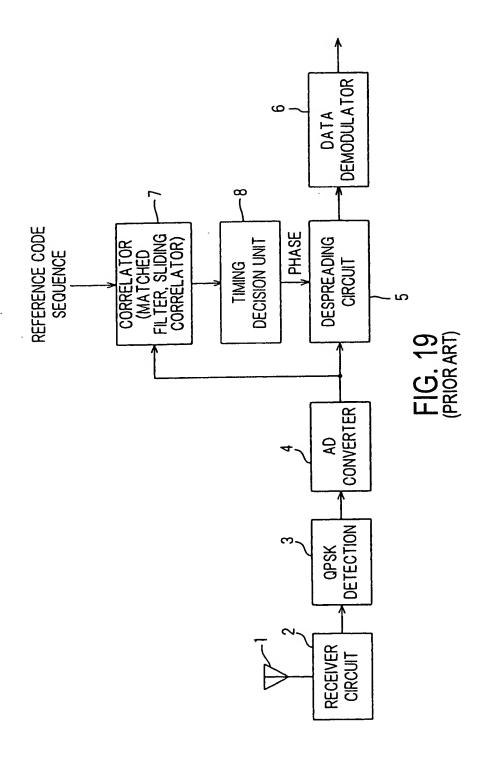


FIG. 18



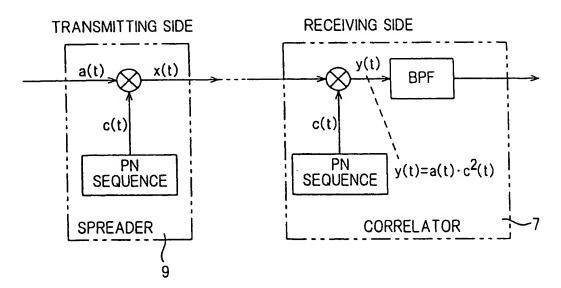
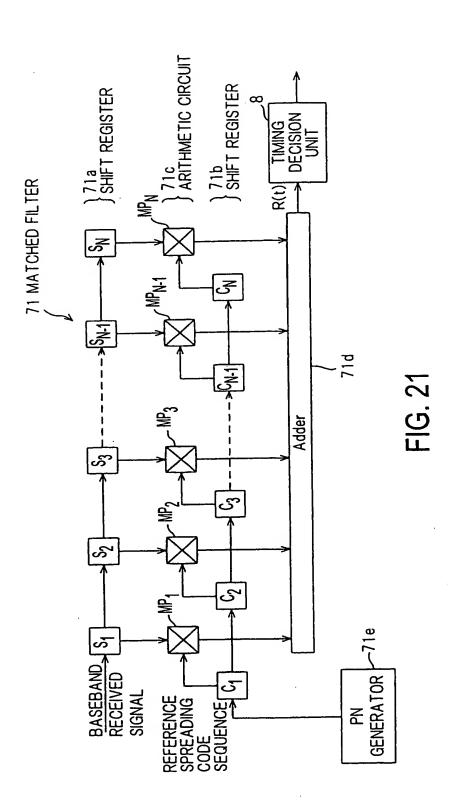


FIG. 20 (PRIOR ART)



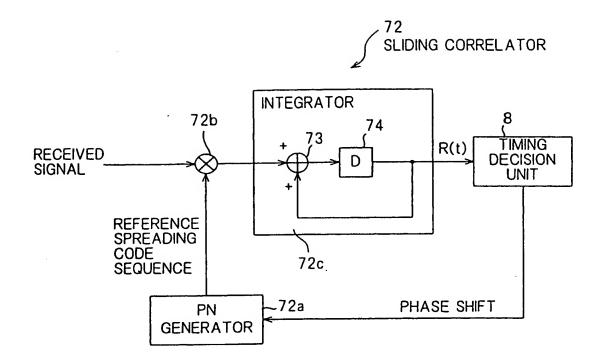


FIG. 22

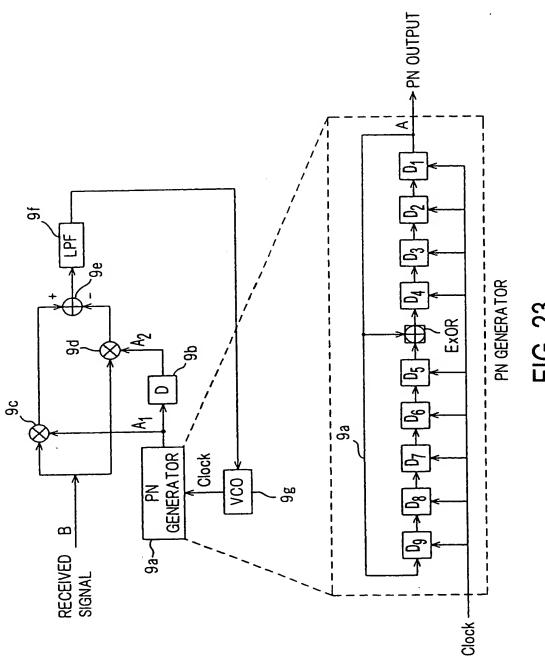


FIG. 23 (PRIOR ART)

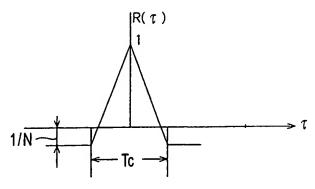


FIG. 24a (PRIOR ART)

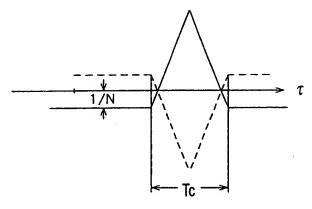


FIG. 24b (PRIOR ART)

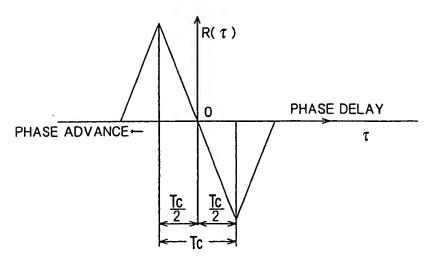


FIG. 24c (PRIOR ART)

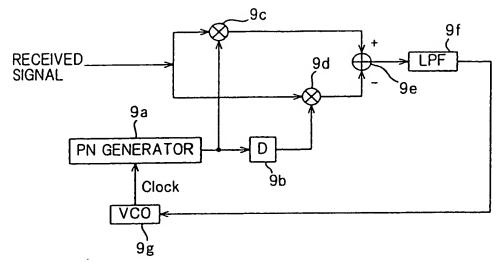


FIG. 25a (PRIOR ART)

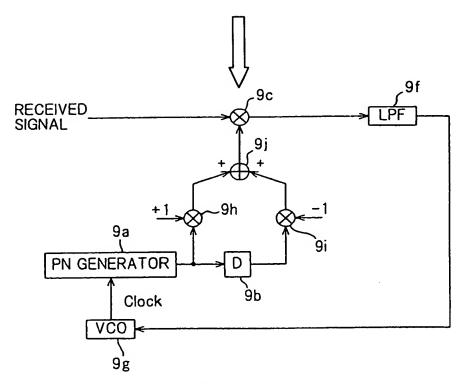


FIG. 25b (PRIOR ART)